Version: 1.2

Total Page: 23

CUSTOMER APPROVAL SHEET

Company Name

MODEL PO120F390MS

CUSTOMER **Title :**

APPROVED **Name :**

☐ **APPROVAL FOR SPECIFICATIONS ONLY (Spec. Ver.____)**

☐ **APPROVAL FOR SPECIFICATIONS AND ES SAMPLE (Spec. Ver.____)**

☐ **APPROVAL FOR SPECIFICATIONS AND CS SAMPLE (Spec. Ver.____)**

☐ **CUSTOMER REMARK :**

1 Li-Hsin Rd. 2. Science-Based Industrial Park
Hsinchu 300, Taiwan, R.O.C.
Tel: +886-3-500-8800
Fax: +886-3-565-1840

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Date :	2017/3/7

Product Specification

1.20" Color AMOLED

MODEL NAME: PO120F390MS

MP product P/N:

< ◆ >Preliminary Specification
< >Final Specification

Note: The content of this specification is subject to change.

Record of Revision

Version	Revise Date	Page	Content
0.0	2016-11-1	1~23	First Draft
0.1	2016-11-8	7	Update sleep in mode data
1.0	2016-11-18	4	Update interface information
		4	Update pin assignment information
		7	Update Display Current Consumption information
		11	Update SPI Interface Characteristics
		15	Update Power Structure & Power on/off Sequence
		17	Update Boost Mode infomation
		20	Update ESD criteria
		23	Update 2D drawing
1.1	2017-1-17	4	Update outline Dimension
1.2	2017-3-7	23	Update packing information

Contents

A. General Specification

1. Physical Specifications

NO	Item	unit	Specification	Remark
1	Screen Size	inch	1.20”	
2	Display Resolution	--	390 x 390	
3	Outline Dimension	mm	33.22 (H) × 34.72 (V) × 0.66(T)	
4	Active Area	mm	30.42 (H) × 30.42 (V)	
5	Color Configuration	--	Hyper R.G.B	
6	Color Depth	--	16.7M	
7	Display Mode	--	AMOLED	
8	Interface	--	MIPI (command mode) + SPI	
9	Display IC		AUO W022 ASIC	

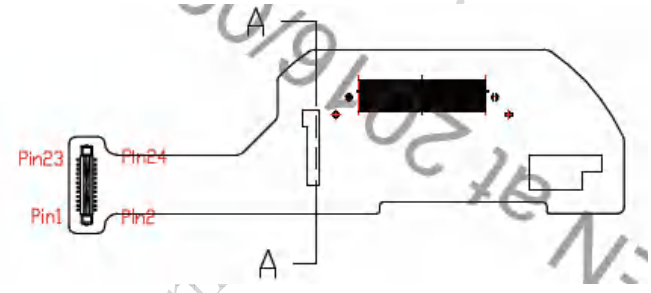
2. Pin Assignment

Main FPC Pin assignment — AMOLED Panel Input/Output Signal Interface

FPCA recommended connector: BM28B0.6-24DP/2-0.35V(51)

Main board recommended connector: BM28B0.6-24DS/2-0.35V(51)

Pin location



Pin assignment

PIN NO.	SYMBOL	DESCRIPTION	REMARK
1	XRES	Device reset signal(0:enable;1:Disable)	
2	VCI_EN	Enable signal for PWR IC control	
3	NC	Floating	
4	GND	Ground	
5	TE	Vsync (vertical sync) signal output from panel to avoid tearing effect	

6	DSI_D0N	MIPI negative data signal	
7	CSX	SPI Enable Signal	
8	DSI_D0P	MIPI positive data signal	
9	SCL	SPI Clock Signal	
10	GND	Ground	
11	DCX	SPI CMD/Data selection signal	
12	DSI_CLKN	MIPI negative clock signal	
13	SDI	SPI data signal	
14	DSI_CLKP	MIPI positive clock signal	
15	SDO	SPI output signal	
16	GND	Ground	
17	NC	Floating	
18	VDDI	Power supply for interface system except MIPI interface	
19	VBAT	Driver analog power supply (Power IC need to follow AUO's suggestion)	
20	VDDI	Power supply for interface system except MIPI interface	
21	VBAT	Driver analog power supply (Power IC need to follow AUO's suggestion)	
22	VBAT	Driver analog power supply (Power IC need to follow AUO's suggestion)	
23	VBAT	Driver analog power supply (Power IC need to follow AUO's suggestion)	
24	VBAT	Driver analog power supply (Power IC need to follow AUO's suggestion)	

3. Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit	Remark
Digital Power Supply	VDDIO	-0.3	5.5	V	
Analog Power Supply	VBAT	-0.3	5.5	V	
ELVDD power Supply	ELVDD	-	5.0	V	
ELVSS power Supply	ELVSS	-5.0	-	V	

Note : If the module exceeds the absolute maximum ratings, it may be damaged permanently. Also, if the module operates with the absolute maximum ratings for a long time, the reliability may drop.

B. DC Characteristics

1. Typical Operating Conditions

Item		Symbol	Min.	Typ.	Max.	Unit	Remark
Digital Power Supply		VDDIO	1.65	1.8	1.95	V	Note1
Analog power Voltage		VBAT	3.27	3.3	3.33	V	Note1
ELVDD power Supply		ELVDD	3.27	3.3	3.33	V	Note1
ELVSS power Supply		ELVSS	-3.33	-3.3	-3.27	V	Note1
Input Signal Voltage	H Level	VIH	0.8* VDDIO	-	VDDIO	V	Note1
	L Level	VIL	0	-	0.2* VDDIO	V	
Output Signal Voltage	H Level	VOH	0.8* VDDIO	-	VDDIO	V	Note1
	L Level	VOL	0	-	0.2* VDDIO	V	Note1

Note 1: The operation is guaranteed under the recommended operating conditions only. The operation is not guaranteed if a quick voltage change occurs during the operation. To prevent the noise, a bypass capacitor must be inserted into the line closed to the power pin.

2. Display Current Consumption

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Panel Power		P_{OLED}	ELVDD:3.3V ELVSS:-3.3V	--	--	128.7	mW	Note1
		I_{OLED}		--	--	19.5	mA	Note1
IC	Normal	P_{VCI}	VCI : 3.3V	--	5.3	--	mW	Note2
		I_{VCI}		--	1.6	--	mA	Note2
		P_{VDDIO}	VDDIO :1.8V	--	3.5	--	mW	Note2
		I_{VDDIO}		--	1.9	--	mA	Note2
	Idle	P_{VCI}	VCI : 3.3V	--	4.5	--	mW	Note3/4
		I_{VCI}		--	1.4	--	mA	Note3/4
		P_{VDDIO}	VDDIO :1.8V	--	2.4	--	mW	Note3/4
		I_{VDDIO}		--	1.3	--	mA	Note3/4
	Sleep	P_{VCI}	VCI : 3.3V	--	0.075<	--	mW	
		I_{VCI}		--	22.6<	--	uA	
		P_{VDDIO}	VDDIO :1.8V	--	0.1<	--	mW	
		I_{VDDIO}		--	55<	--	uA	

Note 1: Based on L255 (350nits) full white pattern

Note 2: Based on black pattern / command mode.

Note 3: Based on black pattern / MIPI - command mode or SPI interface.

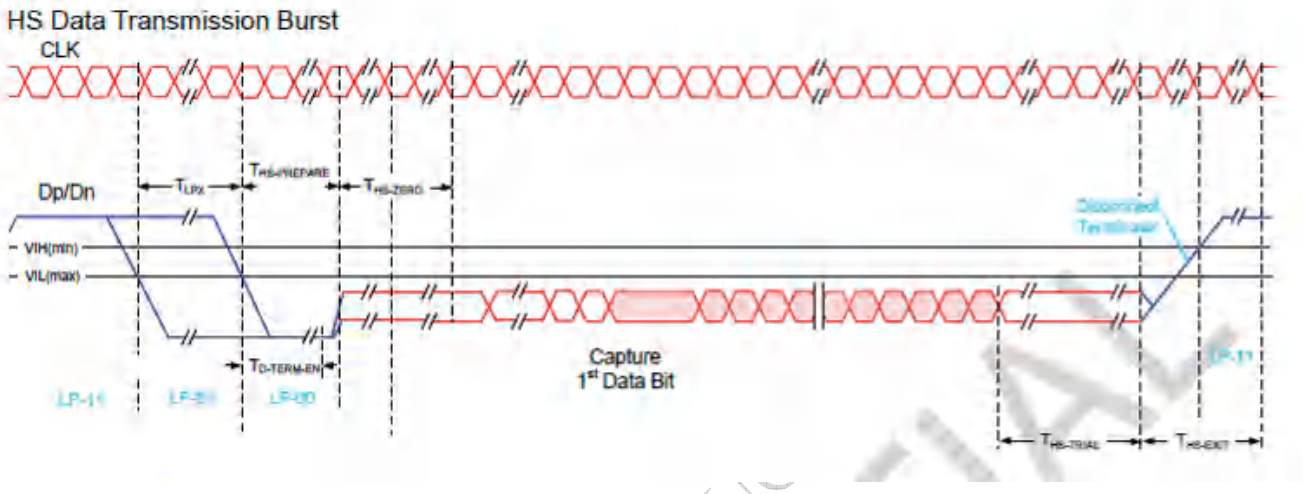
Note 4: IVCI < 8mA at Idle mode.

C. AC Characteristics

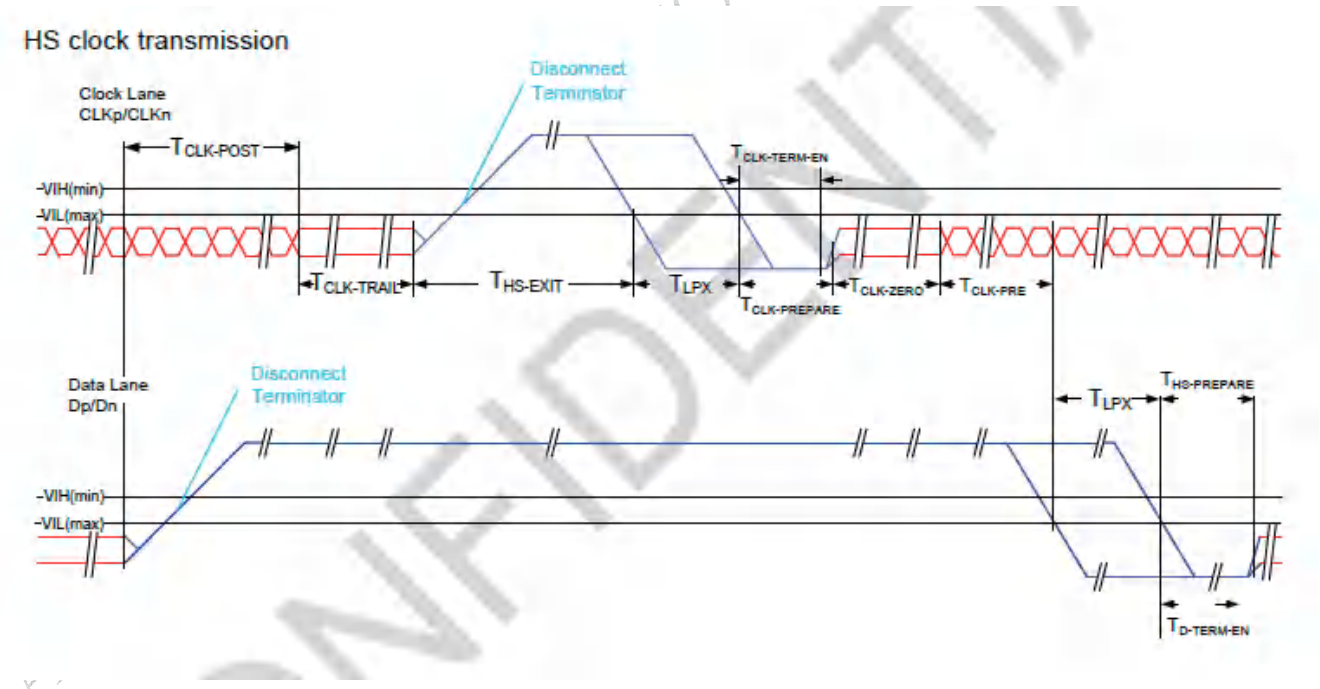
1. MIPI / SPI Interface Characteristics

MIPI Interface Characteristics

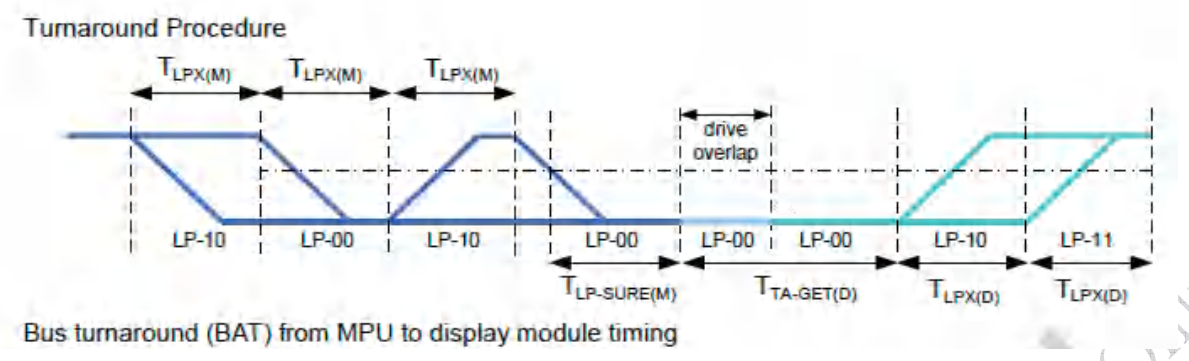
HS Data Transmission Burst



HS clock transmission

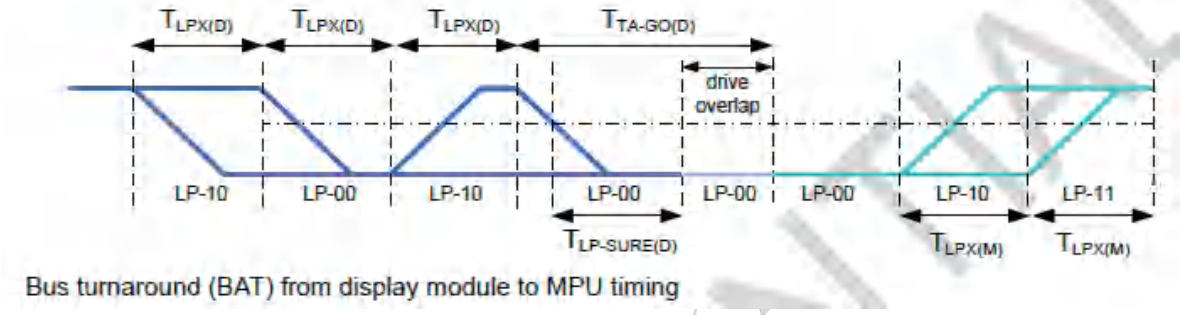


Turnaround Procedure



Bus turnaround (BAT) from MPU to display module timing

Bus turnaround (BAT) from MPU to display module timing



Bus turnaround (BAT) from display module to MPU timing

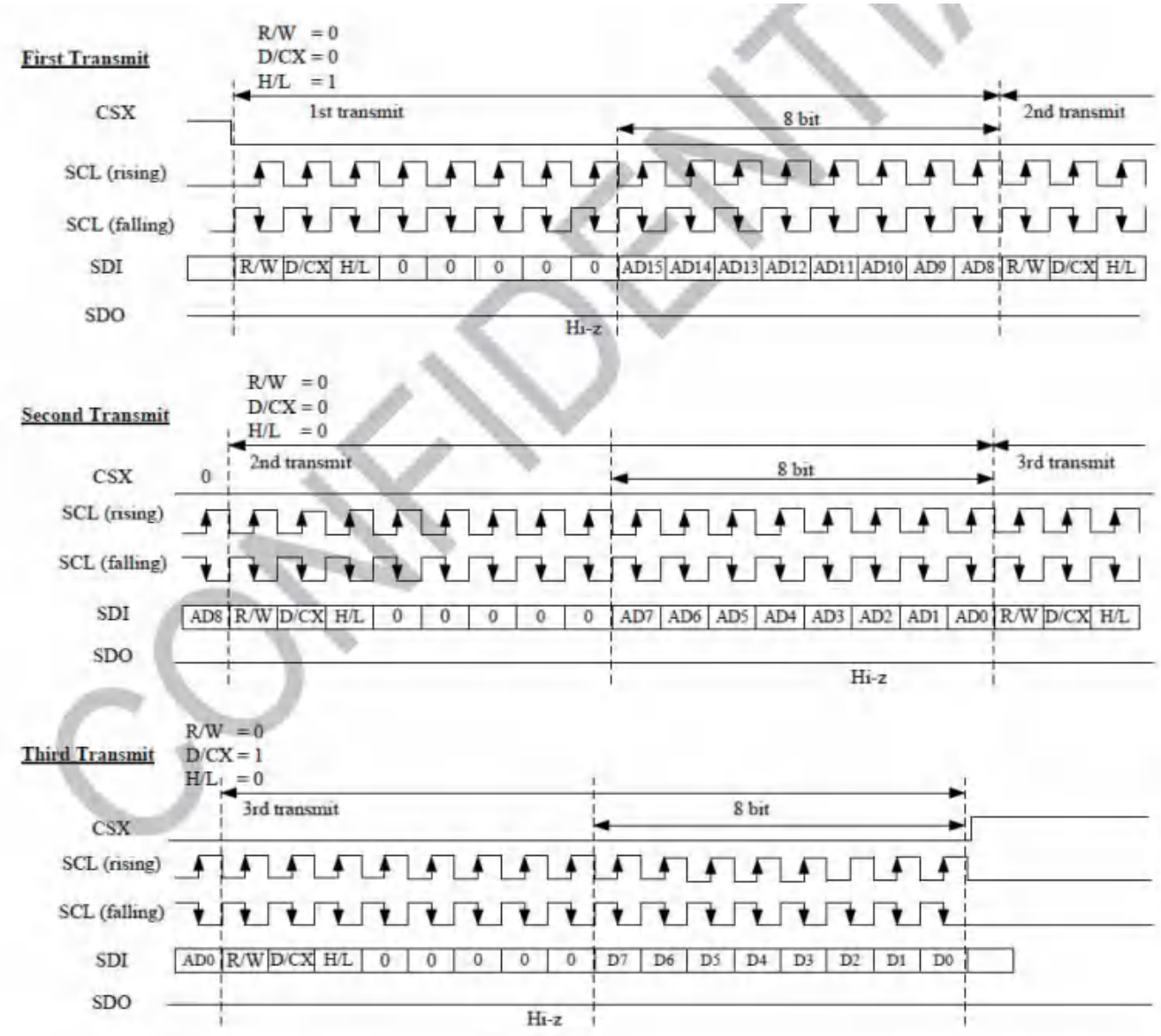
Timing Parameters

Symbol	Description	Min	Typ	Max	Unit
$T_{CLK-POST}$	Time that the transmitter continues to send HS clock after the last associated Data Lane has transitioned to LP Mode. Interval is defined as the period from the end of $T_{HS-TRAIL}$ to the beginning of $T_{CLK-TRAIL}$.	60ns + 52*UI			ns
$T_{CLK-TRAIL}$	Time that the transmitter drives the HS-0 state after the last payload clock bit of a HS transmission burst.	60			ns
$T_{HS-EXIT}$	Time that the transmitter drives LP-11 following a HS burst.	300			ns
$T_{CLK-TERM-EN}$	Time for the Clock Lane receiver to enable the HS line termination, starting from the time point when Dn crosses $V_{IL,MAX}$.	Time for Dn to reach $V_{TERM-EN}$		38	ns
$T_{CLK-PREPARE}$	Time that the transmitter drives the Clock Lane LP-00 Line state immediately before the HS-0 Line state starting the HS transmission.	38		95	ns
$T_{CLK-PRE}$	Time that the HS clock shall be driven by the transmitter prior to any associated Data	8			UI

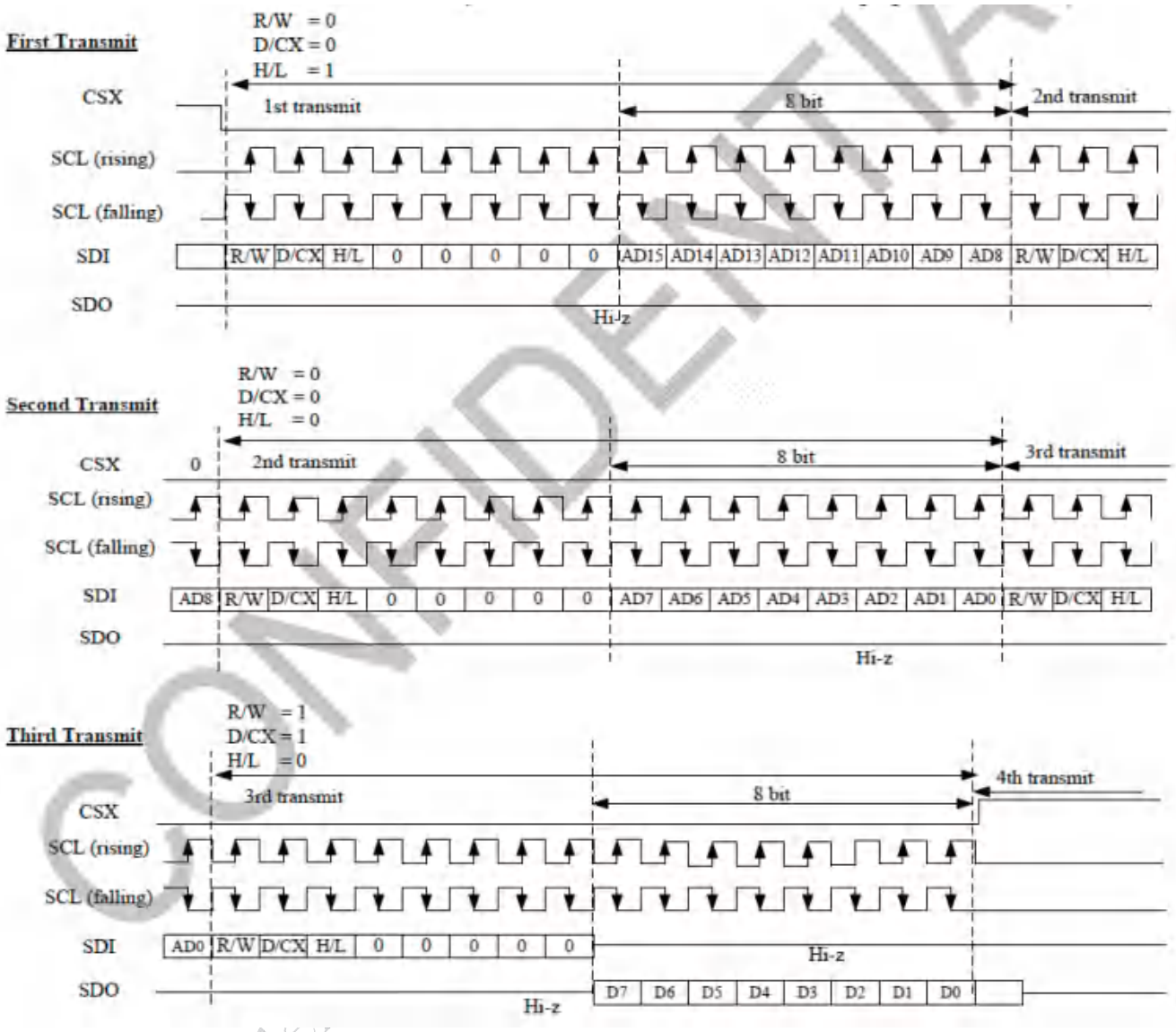
	Lane beginning the transition from LP to HS mode.				
$T_{CLK-PREPARE}$ + $T_{CLK-ZERO}$	$T_{CLK-PREPARE}$ + time that the transmitter drives the HS-0 state prior to starting the Clock.	300			ns
$T_{D-TERM-EN}$	Time for the Data Lane receiver to enable the HS line termination, starting from the time point when Dn crosses $V_{IL,MAX}$.	Time for Dn to Reach $V_{TERM-EN}$		35 ns +4*UI	
$T_{HS-PREPARE}$	Time that the transmitter drives the Data Lane LP-00 Line state immediately before the HS-0 Line state starting the HS transmission	40ns + 4*UI		85 ns + 6*UI	ns
$T_{HS-PREPARE}$ + $T_{HS-ZERO}$	$T_{HS-PREPARE}$ + time that the transmitter drives the HS-0 state prior to transmitting the Sync sequence.	145ns + 10*UI			ns
$T_{HS-TRAIL}$	Time that the transmitter drives the flipped differential state after last payload data bit of a HS transmission burst	60ns + 4*UI			ns
$T_{LPX(M)}$	Transmitted length of any Low-Power state period of MCU to display module	50		150	ns
$T_{TA-SURE(M)}$	Time that the display module waits after the LP-10 state before transmitting the Bridge state (LP-00) during a Link Turnaround.	$T_{LPX(M)}$		2*$T_{LPX(M)}$	ns
$T_{LPX(D)}$	Transmitted length of any Low-Power state period of display module to MCU	50		150	ns
$T_{TA-GET(D)}$	Time that the display module drives the Bridge state (LP-00) after accepting control during a Link Turnaround.		5*$T_{LPX(D)}$		ns
$T_{TA-GO(D)}$	Time that the display module drives the Bridge state (LP-00) before releasing control during a Link Turnaround.		4*$T_{LPX(D)}$		ns
$T_{TA-SURE(D)}$	Time that the MPU waits after the LP-10 state before transmitting the Bridge state (LP-00) during a Link Turnaround.	$T_{LPX(D)}$		2*$T_{LPX(D)}$	ns

SPI Interface Characteristics

Write Cycle in SPI I/F

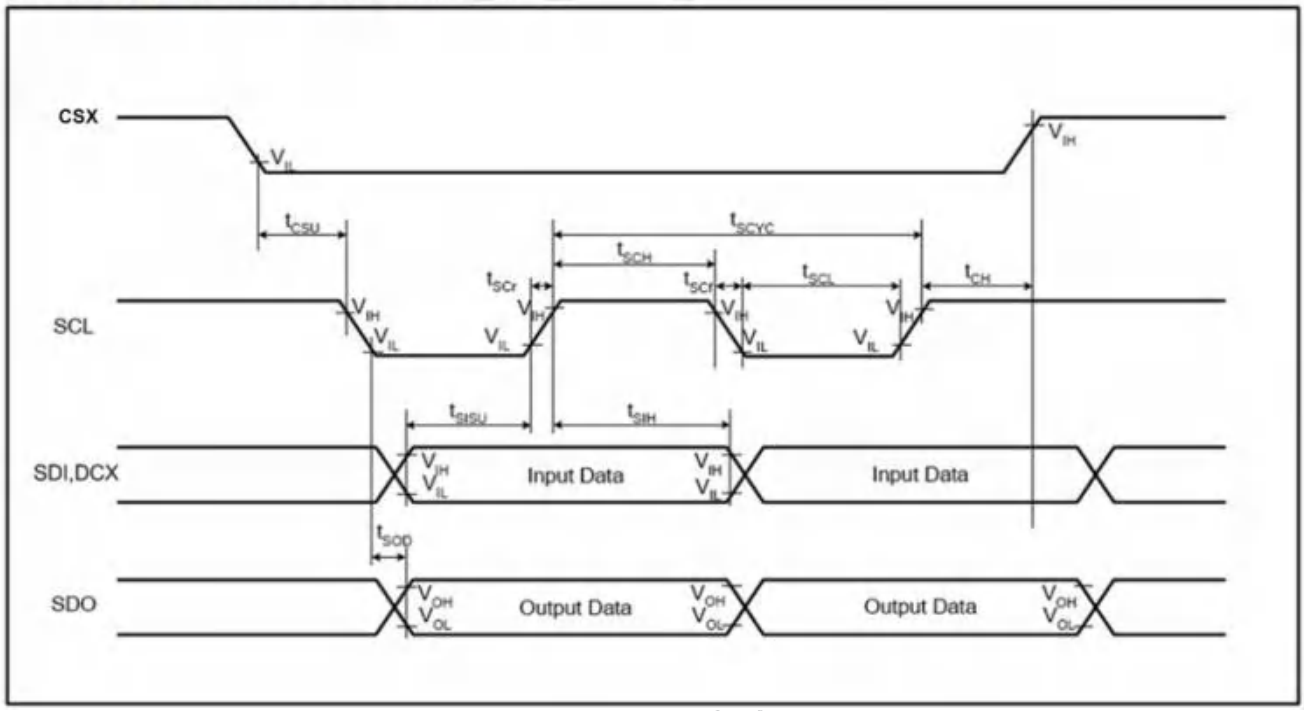


Read Cycle in SPI I/F



Serial Interface Characteristics

4-Wire SPI Serial Interface Characteristics

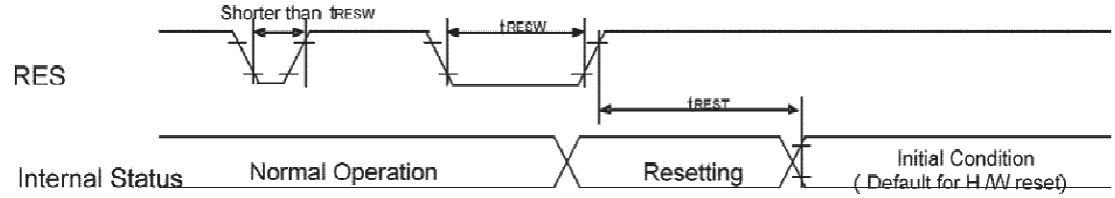


Signal	Symbol	Parameter	MIN	MAX	Unit	Description
SCL	T_{SCYC}	Clock cycle (Write)	20		ns	-
	T_{SCYC}	Clock cycle (Read)	300		ns	
	T_{SCH}	Clock "H" pulse width (Write)	9		ns	
	T_{SCH}	Clock "H" pulse width (Read)	140		ns	
	T_{SCL}	Clock "L" pulse width (Write)	9		ns	
	T_{SCL}	Clock "L" pulse width (Read)	140		ns	
	T_{SCr}	Clock rise time		2	ns	
	T_{SCf}	Clock fall time		2	ns	
CSX	T_{CSU}	Chip select setup time	10		ns	-
	T_{CH}	Chip select hold time	10		ns	
SDI (SDA)	T_{SISU}	Data input setup time	5		ns	-
	T_{SIH}	Data input hold time	5		ns	
SDO (SDA)	T_{SOD}	Data output setup time		120	ns	-
	T_{SOH}	Data output hold time	5		ns	

Note: Logic high and low levels are specified as 20% and 80% of VDDIO for Input signals. Note: Ta = -30 to 70 ℃, VDDIO=1.65V to 3.3V, VCI=2.7V to 3.6V, GND=0V

2. Display RESET Timing Characteristics

Reset input timing



Timing Parameters

Symbol	Parameter	Related Pins	MIN	TYP	MAX	Note	Unit
t_{RESW}	*1) Reset low pulse width	RESX	10	-	-	-	µs
t_{REST}	*2) Reset complete time	-	-	-	5	When reset applied during Sleep in mode	ms
		-		-	120	When reset applied during Sleep out mode	ms

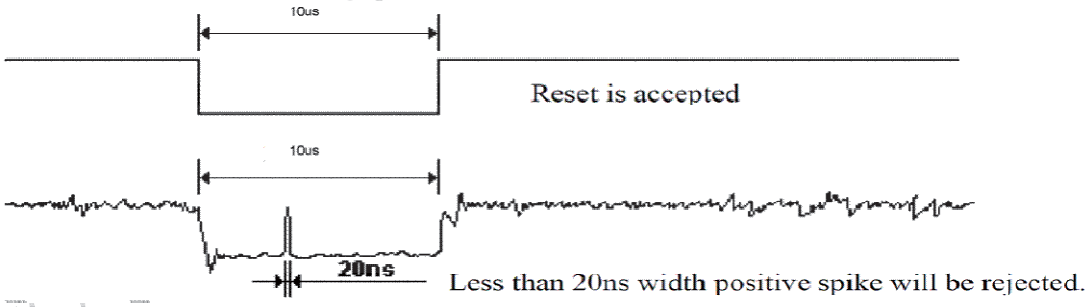
Note 1. Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below.

RESX Pulse	Action
Shorter than 5µs	Invalid Reset
Longer than 10µs	Valid Reset
Between 5µs and 10µs	Reset Initialigation Precedure

Note 2. During the resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out –mode. The display remains the blank state in Sleep In –mode) and then return to Default condition for H/W reset.

Note 3. During Reset Complete Time, data in OTP will be latched to internal register during this period. This loading is done every time when there is H/W reset complete time (tREST) within 5ms after a rising edge of RESX.

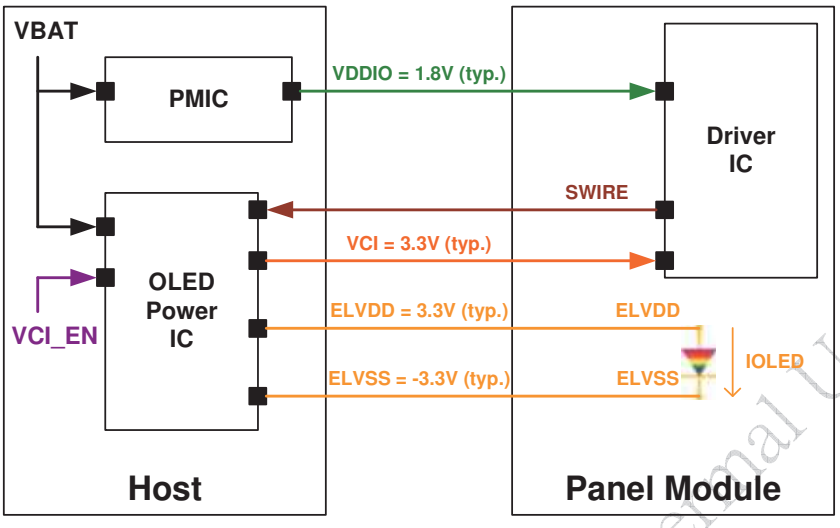
Note 4. Spike Rejection also applies during a valid reset pulse as shown below:



Note 5. It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.

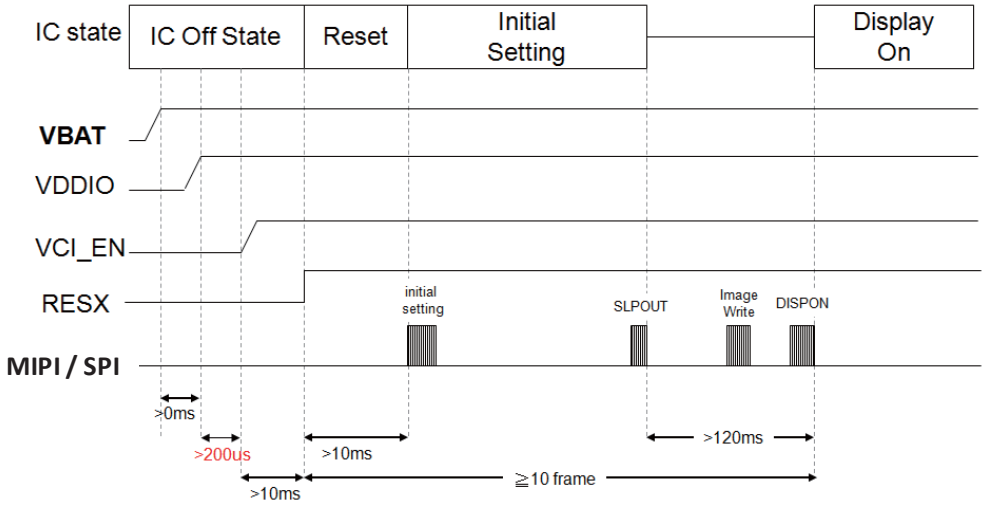
Operating Sequence

Power Structure

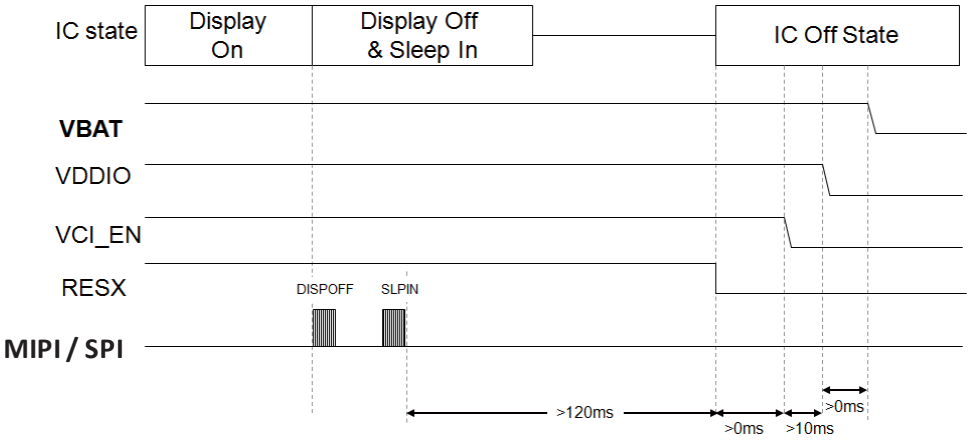


Display Power on/off Sequence

Power on sequence

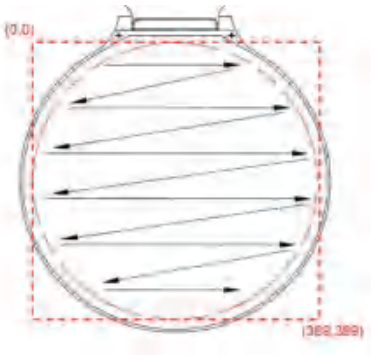


Power off sequence



D. Display Coordinate

Display scan direction



Panel Start Point = (0 , 0)

Panel End Point = (389 , 389)

Coordinate

1	181	269	28	61	54	395	282	121	15	374	360	131	1	388	388	241	5	384	380	301	31	358	328	361	92	297	206
2	171	268	48	62	53	396	284	122	14	375	362	162	0	389	390	242	6	383	378	302	32	357	326	362	94	295	202
3	164	265	62	63	52	397	286	123	14	375	362	183	0	389	390	243	6	383	378	303	32	357	325	363	95	294	200
4	156	231	74	64	51	398	288	124	14	375	362	184	0	389	390	244	6	383	378	304	33	356	324	364	97	292	196
5	153	236	84	65	50	399	290	125	13	376	364	185	0	389	390	245	6	383	378	305	34	355	322	365	99	290	192
6	149	240	92	66	49	340	292	126	12	376	364	186	0	389	390	246	7	382	376	306	34	355	322	366	100	289	190
7	145	244	100	67	48	341	294	127	12	377	366	187	0	389	390	247	7	382	376	307	35	354	320	367	102	287	186
8	141	248	108	68	47	342	296	128	12	377	366	188	0	389	390	248	7	382	376	308	36	353	318	368	104	285	182
9	138	251	114	69	47	342	296	129	12	377	366	189	0	389	390	249	7	382	376	309	36	353	318	369	106	283	178
10	135	254	120	70	46	343	298	130	11	378	368	190	0	389	390	250	8	381	374	310	37	352	316	370	108	281	174
11	132	257	126	71	45	344	300	131	11	378	368	191	0	389	390	251	8	381	374	311	38	351	314	371	110	279	170
12	129	260	130	72	44	345	302	132	11	378	368	192	0	389	390	252	8	381	374	312	39	350	312	372	113	277	166
13	126	263	138	73	43	346	304	133	10	379	370	193	0	389	390	253	9	380	372	313	39	350	312	373	114	275	162
14	124	265	142	74	42	347	306	134	10	379	370	194	0	389	390	254	9	380	372	314	40	349	310	374	117	273	156
15	121	268	148	75	42	347	306	135	10	379	370	195	0	389	390	255	9	380	372	315	41	348	308	375	119	270	152
16	119	270	152	76	41	348	308	136	9	380	372	196	0	389	390	256	10	379	370	316	42	347	306	376	121	268	148
17	117	272	156	77	40	349	310	137	9	380	372	197	0	389	390	257	10	379	370	317	42	347	306	377	124	265	142
18	114	275	162	78	39	350	312	138	9	380	372	198	0	389	390	258	10	379	370	318	43	346	304	378	126	263	138
19	112	277	166	79	39	350	312	139	8	381	374	199	0	389	390	259	11	378	368	319	44	345	302	379	129	260	132
20	110	279	170	80	38	351	314	140	8	381	374	200	0	389	390	260	11	378	368	320	45	344	300	380	132	257	126
21	108	281	174	81	37	352	316	141	8	381	374	201	0	389	390	261	11	378	368	321	46	343	298	381	135	254	120
22	107	283	176	82	36	353	318	142	7	382	376	202	0	389	390	262	12	377	366	322	47	342	296	382	138	251	114
23	104	285	182	83	36	353	318	143	7	382	376	203	0	389	390	263	12	377	366	323	47	342	296	383	141	248	108
24	102	287	186	84	35	354	320	144	7	382	376	204	0	389	390	264	12	377	366	324	48	341	294	384	145	244	100
25	100	289	190	85	34	355	322	145	7	382	376	205	0	389	390	265	13	376	364	325	49	340	292	385	149	240	92
26	99	290	192	86	34	355	322	146	6	383	378	206	0	389	390	266	13	376	364	326	50	339	290	386	153	236	84
27	97	292	196	87	33	356	324	147	6	383	378	207	0	389	390	267	14	375	362	327	51	338	288	387	158	231	74
28	95	294	200	88	32	357	326	148	6	383	378	208	0	389	390	268	14	375	362	328	52	337	286	388	164	225	62
29	94	295	202	89	32	357	326	149	6	383	378	209	0	389	390	269	14	375	362	329	53	336	284	389	171	219	48
30	92	297	206	90	31	358	328	150	5	384	380	210	1	388	388	270	15	374	360	330	54	335	282	390	181	208	28
31	90	299	210	91	30	359	330	151	5	384	380	211	1	388	388	271	15	374	360	331	55	334	280				
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35	84	305	222	95	28	361	334	155	4	385	382	215	1	388	388	275	17	372	356	335	59	330	272				
36	83	306	224	96	27	362	336	156	4	385	382	216	1	388	388	276	17	372	356	336	60	329	270				
37	81	308	228	97	27	362	336	157	4	385	382	217	1	388	388	277	18	371	354	337	61	328	268				
38	80	309	230	98	26	363	338	158	4	385	382	218	1	388	388	278	18	371	354	338	62	327	266				
39	79	310	232	99	26	363	338	159	3	386	384	219	1	388	388	279	19	370	352	339	63	326	264				
40	77	312	236	100	25	364	340	160	3	386	384	220	2	387	386	280	19	370	352	340	64	325	262				
41	76	313	238	101	24	365	342	161	3	386	384	221	2	387	386	281	20	369	350	341	65	324	260				
42	75	314	240	102	24	365	342	162	3	386	384	222	2	387	386	282	20	369	350	342	66	323	258				
43	73	316	244	103	23	366	344	163	3	386	384	223	2	387	386	283	21	368	348	343	67	322	256				
44	72	317	246	104	23	366	344	164	3	386	384	224	2	387	386	284	21	368	348	344	69	320	252				
45	71	318	248	105	22	367	346	165	2	387	386	225	2	387	386	285	22	367	346	345	70	319	250				
46	70	319	250	106	22	367	346	166	2	387	386	226	2	387	386	286	22	367	346	346	71	318	248				
47	69	320	252	107	21	368	348	167	2	387	386	227	3	386	384	287	23	366	344	347	72	317	246				
48	67	322	256	108	21	368	348	168	2	387	386	228	3	386	384	288	23	366	344	348	73	316	244				
49	66	323	258	109	20	369	350	169	2	387	386	229	3	386	384	289	24	365	342	349	75	314	240				
50	65	324	260	110	20	369	350	170	2	387	386	230	3	386	384	290	24	365	342	350	76	313	238				
51	64	325	262	111	19	370	352	171	2	387	386	231	3	386	384	291	25	364	340	351	77	312	236				
52	63	326	264	112	19	370	352	172	1	388	388	232	3	386	384	292	26	363	338	352	79	310	232				
53	62	327	266	113	18	371	354	173	1	388	388	233	4	385	382	293	26	363	338	353	80	309	230				
54	61	328	268	114	18	371	354	174	1	388	388	234	4	385	382	294	27	362	336	354	81	308	228				
55	60	329	270	115	17	372	356	175	1	388	388	235	4	385	382	295	27	362	336	355	83	306	224				
56	59	330	272	116	17	372	356	176	1	388	388	236	4	385	382	296	28	361	334	356	84	305	222				
57	58	331	274	117	17	372	356	177	1	388	388	237	4	385	382	297	29	360	332	357	86	303	218				
58	57	332	276	118	16	373	358	178	1	388	388	238	5	384	380	298	29	360	332	358	87	302	216				
59	56	333	278	119	16	373	358	179	1	388	388	239	5	384	380	299	30	359	330	359	89	300	212				
60	55	334	280	120	15	374	360	180	1	388	388	240	5	384	380	300	30	359	330	360	90	299	210				

E. Optical Specification

Item		Abbr.	Min.	Typ.	Max.	Unit	Remark
Optical Characteristic		Brightness	300	350	--	nits	Note 3
Contrast ratio		@25deg	10000	--	--		Note 4
Brightness Uniformity		350nits	85	--	--		Note 5
Color Temp.		T		7500		K	
Viewing angle CR>1600		Top	80°	--	--	deg	Note 6
		Bottom	80°	--	--	deg	
		Left	80°	--	--	deg	
		Right	80°	--	--	deg	
Color	White	CIE1931 x	0.280	0.300	0.320		Note 7
	White	CIE1931 y	0.290	0.310	0.330		
	Red	CIE1931 x	0.640	0.670	0.700		
	Red	CIE1931 y	0.300	0.330	0.360		
	Green	CIE1931 x	0.186	0.236	0.286		
	Green	CIE1931 y	0.661	0.711	0.761		
	Blue	CIE1931 x	0.090	0.130	0.170		
	Blue	CIE1931 y	0.025	0.065	0.105		
NTSC		CIE x , y	87	100	--	%	
Life time	LT95	25℃	150	--	--	hrs	Note 8
Flicker			--	--	-30	db	Note 9
Gamma		γ	2.0	2.2	2.4		Note 10
Boost mode		Brightness	--	500	--		Note 11

Note 1: Ambient temperature =25 ºC±2 ºC, measured by CA-310

Note 2: To be measured in the dark room.

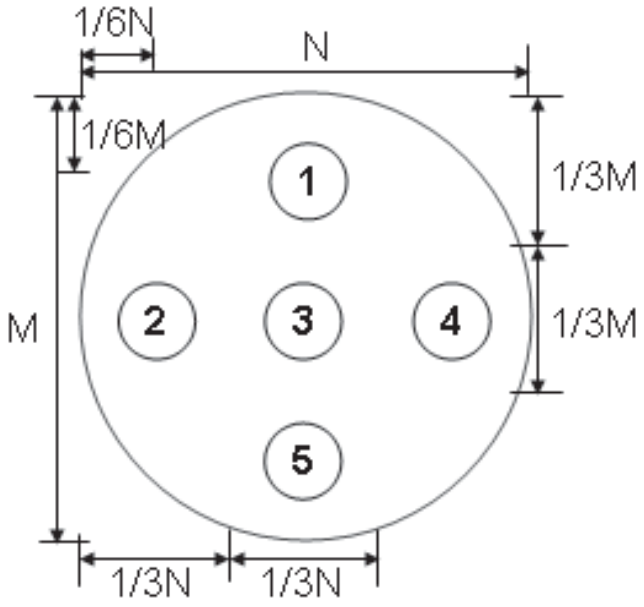
Note 3: The brightness measurement shall be done at the center of the display with a full white image.

Note 4: Definition of contrast ratio:

Contrast ratio is calculated with the following formula:

$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when OLED is at “White” state}}{\text{Photo detector output when OLED is at “Black}}$$

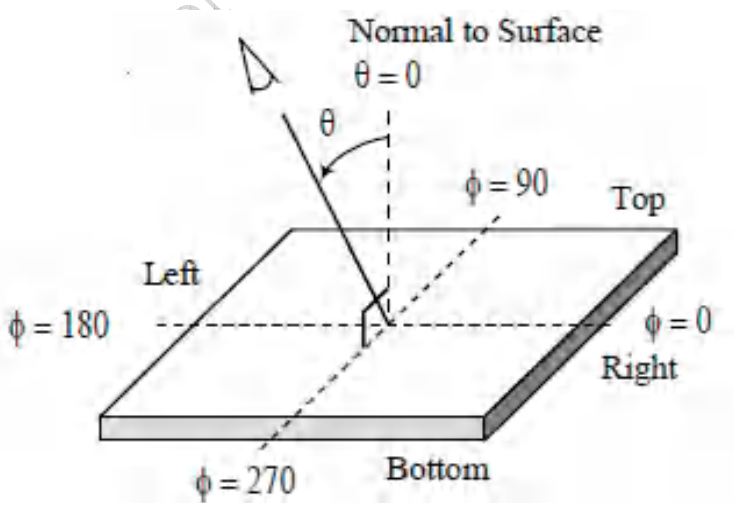
Note 5: Uniformity. Refer to figure as below



- The test condition at 25℃ and measured on the surface of display module
- Measurement equipment: CS2000 or similar equipments
- △Bp = Bp (Min.) / Bp (Max.)×100 (%)
- Bp (Max.) = Maximum brightness in 5 measured spots
- Bp (Min.) = Minimum brightness in 5 measured spots.

Note 6: Definition of viewing angle :

The optical performance is specified as the driver IC located at =270°



Note 7: The color chromaticity should be based on sample performance because new OLED material should be verified later.

Note 8: Time to 95% Luminance

To measure the burn-in effect, a test pattern with white background applied to the AMOLED display at 100% loading

Note 9: Flicker

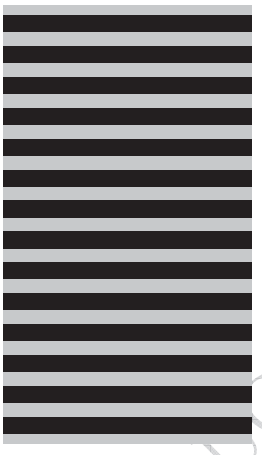
The flicker level is defined using Fast Fourier Transformation (FTT) as follows:

$$Flicker = 20\log_{10}\left(2\frac{f_{FFTC}(n)}{f_{FFTC}(0)}\right) + FS(Hz) \quad (dB)$$

where fFFTC(n) is the nth FFT coefficient, and fFFTC(0) is the 0th FFT coefficient which is DC component. FS(Hz) is the flicker sensitivity as a function of frequency.

The flicker level shall be measured with the test pattern in below.

The gray leves of test pattern is 128.



Note 10 : Gamma spec. is based on Gray level 255, 250, 244, 240, 232, 224, 206, 192, 160, 128, 95, 63, 47 & 31.

Note 11 : Boost mode only guarantee the brightness.

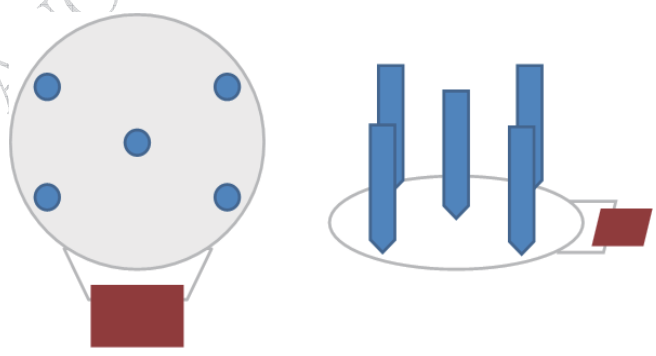
F. Reliability Test Items

Category	No.	Test items	Conditions		Remark
Reliability (Environment)	1	High Temp. Operation	Ta= 70ºC	240 hrs	Reliability (Environment)
	2	High Temp. Storage	Ta= 80 ºC	240 hrs	
	3	Low Temp. Operation	Ta= -20 ºC	240 hrs	
	4	Low Temp. Storage	Ta= -30 ºC	240 hrs	
	5	High Temp./Humi. Operation	Ta= 60 ºC. 90% RH	120 hrs	
	6	High Temp./Humi. Storage	Ta= 60 ºC. 90% RH	240 hrs	
	7	Thermal Shock	-30 ºC ~70 ºC, Dwell for 30 min. 100 cycles		Non-operation
RELIABILITY	8	Electrical Static Discharge	Contact: ±4kV, 150pF/330Ω（power on. Each edge selected one position, a total of 5 position） Air: ±8kV, 150pF/330Ω（power on. Each edge selected one position, a total of 5 position）		Note 11
	9	Box Vibration / Drop	Random Vibration : 1 corner, 3 Edges, 6 Surfaces		
	10	FPC Peeling Test	pull stress＞5N /cm speed 50mm/min, 90°， FPC no peel off, Peeling direction: 90°; Velocity:50mm/min.  Pulling upward by 90 °		

Judge Criteria: No functional defect.

Note 11:

ESD position refer to figure as below



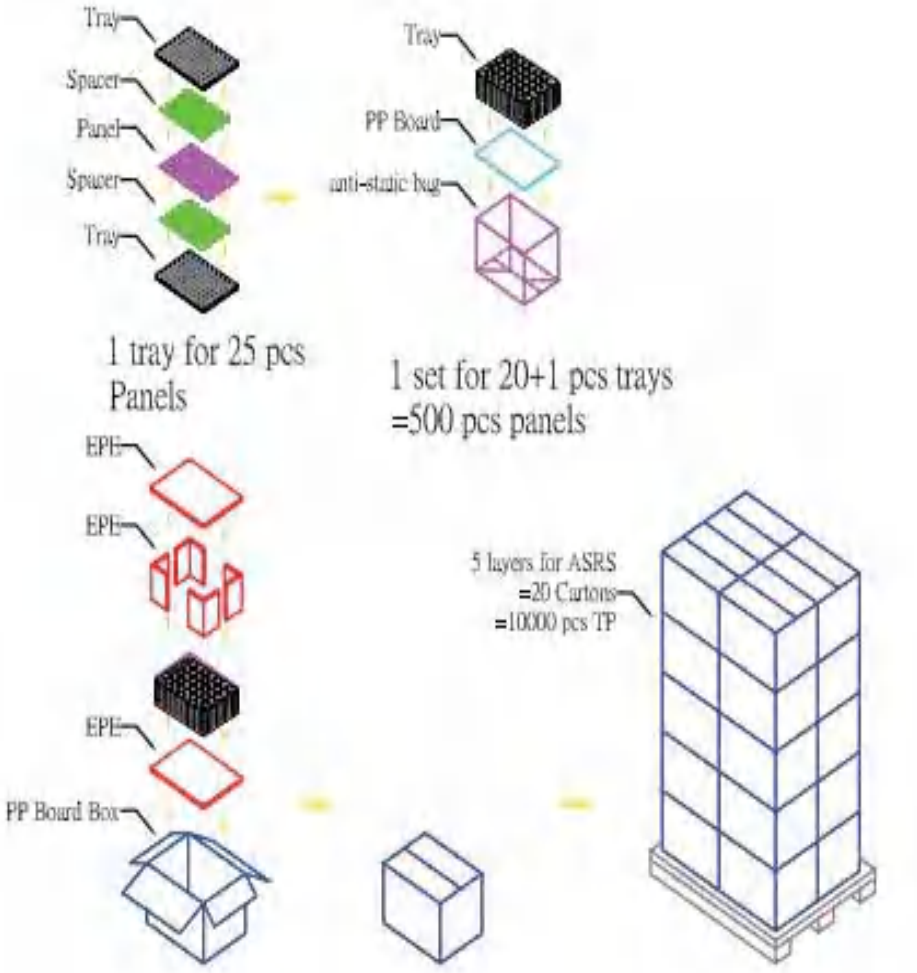
Each panel 5 point, 1 point 5 times

G. Precautions

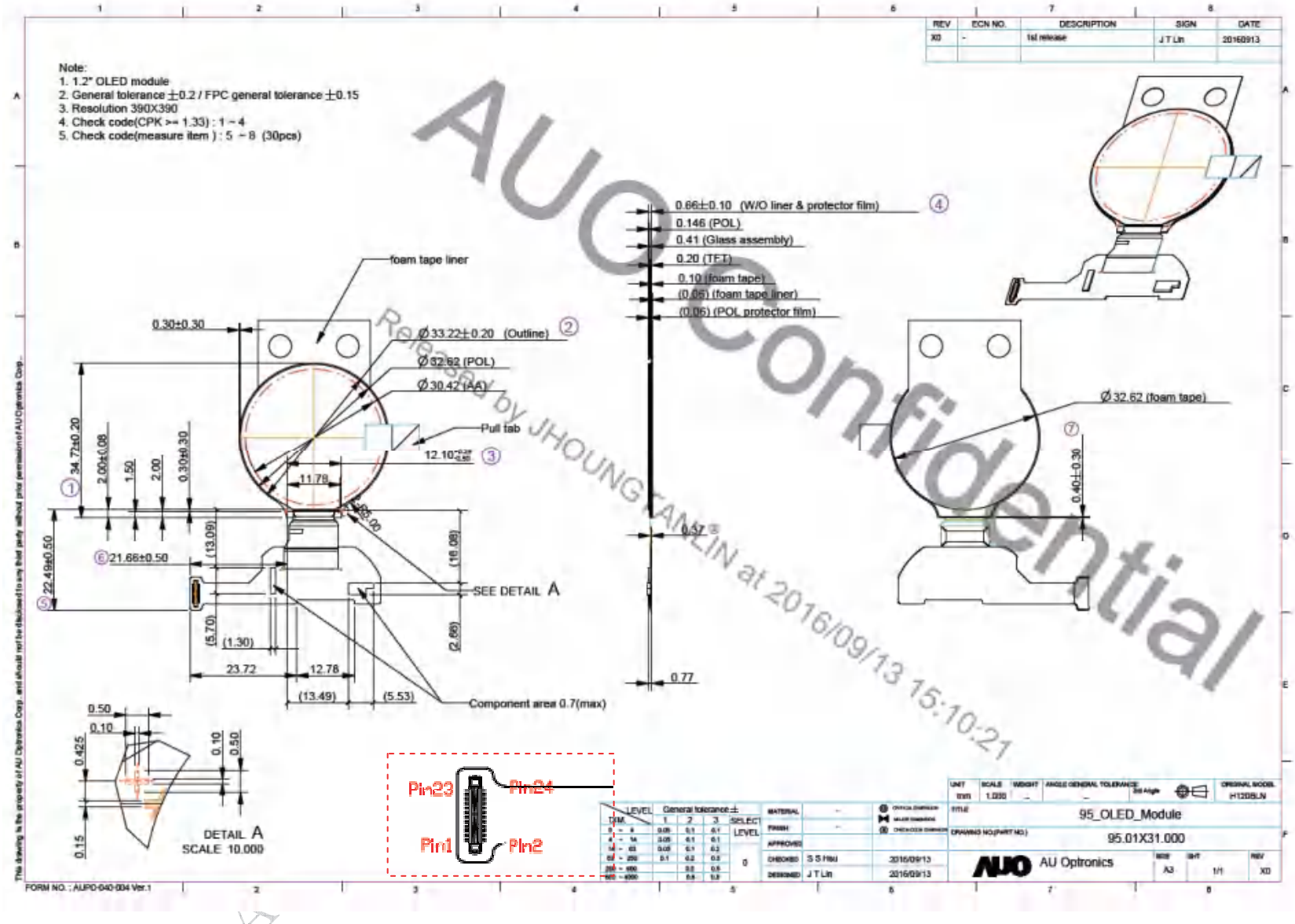
Please pay attention to the following items when you use the OLED Modules(Panel):

1. Do not twist or bend the module(panel) and prevent the unsuitable external force for display during assembly.
2. Adopt measures for good heat radiation. Be sure to use the module(panel) with in the specified temperature.
3. Avoid dust or oil mist during assembly.
4. Follow the correct power sequence while operating. Do not apply the invalid signal, otherwise, it will cause improper shut down and damage the module(panel).
5. Less EMI: it will be more safety and less noise.
6. Please operate module(panel) in suitable temperature. The response time & brightness will drift by different temperature.
7. Avoid to display the fixed pattern (exclude the white pattern) in a long period, otherwise, it will cause image sticking.
8. Please be sure to turn-off the power when connecting or disconnecting the circuit.
9. Polarizer scratches easily, please handle it carefully.
10. Display surface never likes dirt or stains.
11. A dew drop may lead to destruction. Please wipe off any moisture before using module(panel).
12. Sudden temperature changes cause condensation, and it will cause polarizer damaged.
13. High temperature and humidity may degrade performance. Please do not expose the module(panel) to the direct sunlight and so on.
14. Acetic acid or chlorine compounds are not friends with AMOLED display module(panel).
15. Static electricity will damage the module(panel), please do not touch the module(panel) without any grounded device.
16. Please avoid any static electricity damage (ESD) during producing and operating.
17. Do not disassemble and reassemble the module(panel) by self.
18. Be careful do not touch the rear side directly.
19. No strong vibration or shock. It will cause module(panel) broken.
20. Storage the modules(panel) in suitable environment with regular packing.
21. Be careful of injury from a broken display module(panel).
22. Please avoid the pressure adding to the surface (front or rear side) of modules(panel), because it will cause the display non-uniformity or other function issue.

H. Packing Information



I. Outline Dimension



95.01X31.000_X120
BLN01_95_drawing_